

Notice of References Cited	Application/Control No. 10/581,641		Applicant(s)/Patent Under Reexamination BRIL ET AL.	
	Examiner BRIAN CHEW		Art Unit 2195	Page 1 of 1

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*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-5,826,082	10-1998	Bishop et al.	718/104
	B	US-			
	C	US-			
	D	US-			
	E	US-			
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	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

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NON-PATENT DOCUMENTS

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	U	Narlikar, J. G. Space-Efficient Implementation of Nested Parallelism, ACM SIGPLAN Notices, Association for Computing Machinery, New York, Vol. 32, no. 7 (Jul. 1, 1997), pp. 25-36
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
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